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SPECIAL ISSUE

TERAHERTZ RF ELECTRONICS AND SYSTEM INTEGRATION

Edited by T. W. Crowe, W. R. Deal, M. Schröter, C.-K. C. Tzuang, and K. Wu

990 THz Diode Technology: Status, Prospects, and Applications

By I. Mehdi, J. V. Siles, C. Lee, and E. Schlecht

INVITED PAPER This paper describes the current status of GaAs Schottky diode technology including operation, performance, and application to mixers and multipliers.

1008 Status and Prospects of High-Power Heterostructure Barrier **Varactor Frequency Multipliers**

By J. Stake, A. Malko, T. Bryllert, and J. Vukusic

INVITED PAPER The paper provides an overview of the heterostructure barrier varactor (HBV) and its application to frequency multipliers.

1020 Advanced Terahertz Sensing and Imaging Systems Based on **Integrated III-V Interband Tunneling Devices**

By L. Liu, S. M. Rahman, Z. Jiang, W. Li, and P. Fay INVITED PAPER This paper describes a novel diode for detector applications at submillimeter-wave frequencies.

1035 Si/SiGe:C and InP/GaAsSb Heterojunction Bipolar Transistors for **THz Applications**

By P. Chevalier, M. Schröter, C. R. Bolognesi, V. d'Alessandro, M. Alexandrova, J. Böck, R. Flückiger, S. Fregonese, B. Heinemann, C. Jungemann, R. Lövblom, C. Maneux, O. Ostinelli, A. Pawlak, N. Rinaldi, H. Rücker, G. Wedel, and T. Zimmer INVITED PAPER This paper describes Si/SiGe:C and InP/GaAsSb heterojunction bipolar transistors (HBTs), and provides information on thermal and substrate effects, reliability, and radio-frequency (RF) performance.

1051 InP HBT Technologies for THz Integrated Circuits

By M. Urteaga, Z. Griffith, M. Seo, J. Hacker, and M. J. W. Rodwell INVITED PAPER This paper describes the operation and scaling of InP heterojunction bipolar transistors (HBTs) to terahertz frequencies.

1068 SiGe HBT Technology: Future Trends and TCAD-Based Roadmap

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INVITED PAPER This paper presents a technology roadmap for the electrical performance of high-speed silicon-germanium (SiGe) heterojunction bipolar transistors (HBTs).

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By N. S. Barker, M. Bauwens, A. Lichtenberger, and R. Weikle, II INVITED PAPER This paper considers the development of terahertz systems-on-chip using micromachining techniques based on silicon-on-insulator technology.

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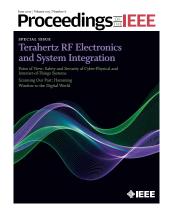
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